



IFW 2631  
PATENT  
536-009.014

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

**A. TROYA et al.**

Application No.: **10/521,396** : Group No.: **2631**  
Filed: **January 14, 2005** : Examiner: **To Be Assigned**

For: ***Method and device for frame detection and synchronizer***

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Applicant submits herewith references of which they are aware, which they believe may be material to the examination of this application and in respect of which they may have a duty to disclose in accordance with 37 CFR 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any document referred to herein is "prior art" for this invention unless specifically designated as such.

I hereby certify that this correspondence is being deposited with the United States Postal Service on this date, **March 20, 2006**, with sufficient postage as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
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Lissette Ramos

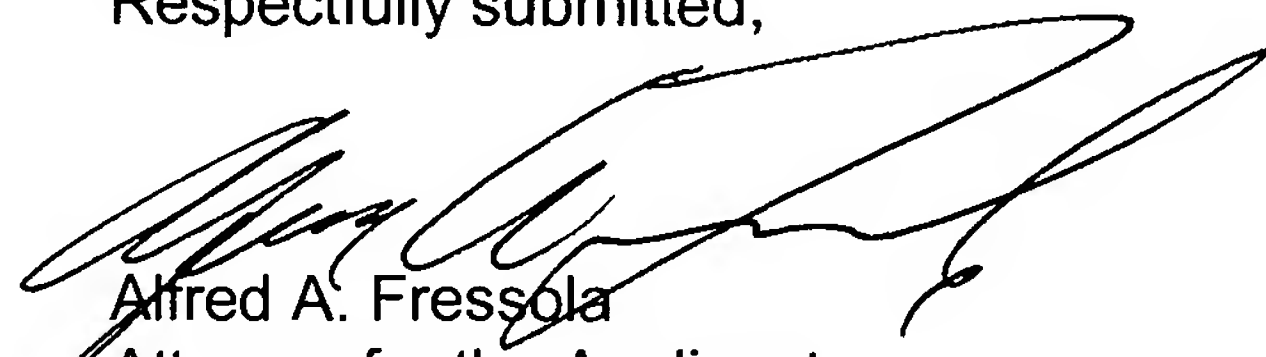
In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined under 37 CFR 1.56(a) exists.

Enclosed is a European Search Report dated February 14, 2006, issued in corresponding European Patent Application No. EP 03763880.

Also enclosed is a Form PTO-1449 listing the cited references. Copies of the cited non-US references and articles are enclosed herewith. A copy of the cited US reference is not enclosed pursuant to the USPTO waiver of the requirement to submit copies of cited US references. The relevance of each reference is specifically explained in the European Search Report and the abstract of each reference provides a concise explanation thereof.

This IDS is being submitted within three months of the mailing date of the European Search Report; therefore, the undersigned respectfully submits that no fee is due for filing this IDS. The Commissioner is hereby authorized to charge to deposit account 23-0442 any fee deficiency required to submit this IDS.

Respectfully submitted,



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Dated: March 20, 2006

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**FORM PTO-1449**  
**INFORMATION DISCLOSURE STATEMENT**


ATTY DOCKET NO.

536-009.014

SERIAL NO.

10/521,396

APPLICANT: Alfonso TROYA et al.

FILING DATE:

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ART UNIT:

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**UNITED STATES PATENT DOCUMENTS**

EXAM. INITIAL	DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,320,917	Nov. 20, 2001	<i>Stott et al.</i>			

**FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
WO 00/77961	Dec. 21, 2000	PCT			
EP 1071251	Jan. 24, 2001	Europe			

**OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)**

	"Orthogonal frequency division multiplex synchronization techniques for frequency-selective fading channels;" T. Keller et al; IEEE Journal on Selected Areas in Communications; vol. 19, no. 6, pages 999-1008, June 2001.
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	"OFDM with trailing zeros versus OFDM with cyclic prefix: Links, comparison and application to the Hiperlan/2 system;" B. Muquet et al; 2000 IEEE International conference on communications; vol. 2 of 3, pages 1049-1053, June 18, 2000.
	"Performance of an OFDM transceiver for radio transmission in industrial multipath environments;" A. Koukourgiannis et al; VTC 2001 Spring, IEEE Vehicular technology conference; vol. 2 of 4, pages 805-809, May 6, 2001.
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	"Design considerations for minimal-power wireless spread spectrum circuits and systems;" B.A. Myers et al; Proceedings of the IEEE; vol. 88, no. 10, pages 1598-1612; October 10, 2000.
	"Gated clock routing 30 for low-power microprocessor design;" Oh Aewon et al; IEEE transactions on computer-aided design of integrated circuits and systems; vol. 20, no. 6, pages 715-722; June 2001.

Examiner (To be assigned)

Date: